

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Masaya SUMITA, et al.

Serial No.: 10/689,554

Filed: October 21, 2003

For: SEMICONDUCTOR INTEGRATED CIRCUIT APPARATUS



: Customer Number: 20277

: Confirmation Number: 9311

: Group Art Unit: 2812

: Examiner: To be Assigned

REQUEST FOR CORRECTED FILING RECEIPT


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Commissioner for Patents
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Sir:

Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office in the above-referenced application. It is noted that the third Inventor information was omitted. Attached is a copy of the Declaration and Power of Attorney Document, which evidences that the third Inventor information should read: Masayoshi KINOSHITA. It is requested that a corrected filing receipt be issued.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

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Registration No. 36,139

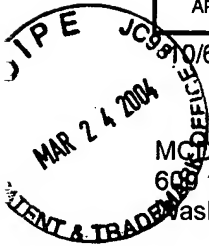
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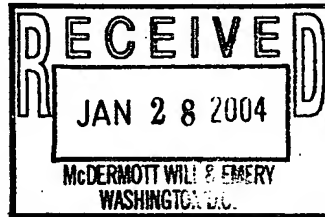
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| 10/689,554 | 10/21/2003 | 2812 | 0.00 | 61282-039 | 34 | 28 | 1 |



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CONFIRMATION NO. 9311

FILING RECEIPT



OC000000011766370

Date Mailed: 01/26/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Masaya Sumita, Residence Not Provided;
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Masayoshi KINOSHITA

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2002-306138 10/21/2002
JAPAN 2003-358891 10/20/2003

If Required, Foreign Filing License Granted: 01/26/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor integrated circuit apparatus

Preliminary Class

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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|---|----------------------|
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